

Form PTO-1449  
(Rev. 8-83)U.S. Department of Commerce  
Patent and Trademark Office

Atty. Docket No.: 0756-1603

Serial No. 08/841,644

## INFORMATION DISCLOSURE STATEMENT

Applicant: Shunpei Yamazaki et al.

(Use several sheets if necessary)

SEP 24 1997

Filing Date: April 30, 1997

Group: 2503

## U.S. PATENT DOCUMENTS

Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if approp.)
UR	*4,469,568	09/04/84	Kato et al.	—	—	
UR	*4,727,044	02/23/88	Yamazaki	—	—	
UR	*5,165,075	11/17/92	Hiroki et al.	—	—	
UR	5,289,030	02/22/94	Yamazaki et al.	—	—	
UR	*5,474,942	12/12/95	Kodaira et al.	—	—	
UR	5,495,121	02/27/96	Yamazaki et al.	—	—	
UR	5,592,008	01/07/97	Yamazaki et al.	—	—	
CP	5,596,214	01/21/97	Endo	—	—	
CP	5,177,568	01/05/93	Honma et al.	—	—	
UR	4,468,855	09/04/84	Sasaki	—	—	
UR	4,557,036	12/10/85	Kyuragi et al.	—	—	

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Document Number	Date	Country	Class	Subclass	Translation Yes No
*55-024420	02/21/80	Japan	—	—	
*57-087175	05/31/82	Japan	—	—	
*58-115864	07/09/83	Japan	—	—	
*61-105873	05/23/86	Japan	—	—	
*61-147574	07/05/86	Japan	—	—	

## OTHER DOCUMENTS (Including Author, Title, Relevant Pages, Date, Place of Publication)

UR	Wolf et al., Silicon Processing, Lattice Press, Vol. 1, 1986, pp. 151-154
UR	Wolf et al., Silicon Processing, Lattice Press, 1990, Vol. 2, pp. 66-72.
UR	S. Wolf & R. N. Tauber, "Silicon Processing for the VLSI Era" vol. 1, 1986, pp. 177-180, 198, 187-188, 335. Translation of JP 61-147574 (Imashiro).

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Date Considered

4/1/88

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UR	4,905,066	02/27/90	Kohjo et al.	—	—	—
UR	4,746,628	05/24/88	Takafuji et al.	—	—	—
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UR	4,646,426	03/03/87	Sasaki	—	—	—
UR	4,900,695	02/13/90	Takahashi et al.	—	—	—
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UR	4,868,137	09/19/89	Kubota	—	—	—
UR	4,769,338	09/06/88	Ovshinsky et al.	—	—	—
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UR	*4,761,058	08/02/88	Okubo et al.	—	—	—
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*61-156885	07/16/86	Japan	—	—	
*61-185724	08/19/86	Japan	—	—	
*61-252667	11/10/86	Japan	—	—	
*63-178560	07/22/88	Japan	—	—	
*1-185522	07/25/89	Japan	—	—	

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UR	S. Morozumi, et al., R&D Department, Seiko Epson Corporation, Negano, "5.2 Low Temperature Processed Poly Si TFT and its Application to Large Area LCD", Japan Display '86, pp. 196 to 199.
UR	K.Nakazawa et al., "Lightly Doped Drain TFT Structure for Poly-Si LCDs", SID '90 Digest, pp. 311-314.
UR	"Enhanced Adhesion To Glass Substrates Using Tantalum Oxide Adhesion Layer", Research Disclosure, No. 325, May 1991, Havant GB, p. 369, XP229725.

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Examiner Initial	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date (if approp.)
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CR	*3,275,915	09/27/66	Harendza	—	—	
CR	*5,132,821	07/21/92	Nicholas	—	—	
CR	*4,470,852	09/11/84	Ellsworth	—	—	
CR	*4,731,642	03/15/88	Katto et al.	—	—	
CR	*4,692,994	09/15/87	Moniwa et al.	—	—	
CR	*4,167,018	09/04/79	Ohba et al.	—	—	
CR	*5,124,823	06/23/92	Kawasaki et al.	—	—	
CR	*5,166,816	11/24/92	Kaneko et al.	—	—	
CR	5,474,945	12/12/95	Yamazaki et al.	—	—	

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Document Number	Date	Country	Class	Subclass	Translation Yes No
*1-183853	07/21/89	Japan	—	—	
*2-85826	03/27/90	Japan	—	—	
*2-90683	03/30/90	Japan	—	—	
*2-148831	06/07/90	Japan	—	—	
*2-306664	12/20/90	Japan	—	—	

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							Yes	No
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UR		*3-108319	05/08/91	Japan	—	—		
UR		58-32466	02/25/83	Japan	—	—		
UR		58-124273	07/23/83	Japan	—	—		
UR		59-214262	12/04/84	Japan	—	—		

**OTHER DOCUMENTS** (Including Author, Title, Relevant Pages, Date, Place of Publication)


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UR	58-115864	07/09/83	Japan	—	—		
UR	62-124769	06/06/87	Japan	—	—		
UR	*1-222468	09/05/89	Japan	—	—		
UR	*2-260540	10/23/90	Japan	—	—		
UR	*2-238666	09/20/90	Japan	—	—		

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